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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/716,475	11/20/2003	Frank Ilchmann	Q78453	6864	
23373	7590 07/06/2005		EXAMINER		
	MION, PLLC	CHANG, DANIEL D			
2100 PENNSYLVANIA AVENUE, N.W. SUITE 800			ART UNIT	PAPER NUMBER	
WASHINGTON, DC 20037			2819		
			DATE MAILED: 07/06/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

Supplemental
Notice of Allowability

Application No.	Applicant(s)
10/716,475	ILCHMANN, FRANK
Examiner	Art Unit
Daniel D. Chang	2819

Examiner	Art Unit	
Daniel D. Chang	2819	
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<u>5/05</u> .		
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5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☑ Examiner's Amendn	atent Application (PT0 (PTO-413), e nent/Comment	,
	Daniel D. Chang  Pars on the cover sheet with the co (OR REMAINS) CLOSED in this application is subject to and MPEP 1308.  5/05.  Examiner.  Inder 35 U.S.C. § 119(a)-(d) or (f).  Inder been received.  Inder been received in Application No.  Cuments have been received in this in the communication to file a reply denote the series of this application.  Inder 35 U.S.C. § 119(a)-(d) or (f).  Inder 35 U.S.C. § 119	Daniel D. Chang  Daniel D. In the continuation of include and include and included

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Brian Shelton on July 1, 2005.

The application has been amended as follows:

Regarding Claim 1, lines 1-2, the recitation, "for converting ECL signals to CMOS signals, said converter" has been deleted.

On line 3, the recitation, --receiving ECL signals-- has been added after "an input stage".

On line 6, the recitation, --converting said ECL signals to CMOS signal-- has been added after "the level shifter stage".

Regarding Claim 5, line 2, the recitation, "for converting ECL signals to CMOS signals" has been deleted.

On line 3, the recitation, --receiving ECL signals-- has been added after "an input stage".

On line 6, the recitation, --converting said ECL signals to CMOS signal-- has been added after "the level shifter stage".

## Reasons for Allowance

Claims 1 and 5-7 are allowable over the prior art of record.

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The following is an examiner's statement of reasons for allowance: the best prior art of record, Maples, taken alone or in combination of other references, does not teach or fairly suggest a converter comprising, among other things, an input stage receiving ECL signals and an output stage connected to the level shifter stage converting said ECL signals to CMOS signals, as set forth in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## **Contact Information**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (571) 272-1801. The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Daniel D. Chang Primary Examiner Art Unit 2819

DANIEL CHANG PRIMARY EXAMINER

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